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PATENT APPLICATION

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IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Christopher A. Poirier et al.

Confirmation No.: 7519

Application No.: 10/544,625

Examiner: S. Whitmore

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Group Art Unit: 2825

Title: A SYSTEM FOR AND METHOD OF CONTROLLING A VLSI ENVIRONMENT

Mail Stop Appeal Brief - Patents  
Commissioner For Patents  
PO Box 1450  
Alexandria, VA 22313-1450

TRANSMITTAL OF REPLY BRIEF

Transmitted herewith is the Reply Brief with respect to the Examiner's Answer mailed on May 18, 2007.

This Reply Brief is being filed pursuant to 37 CFR 1.193(b) within two months of the date of the Examiner's Answer.

(Note: Extensions of time are not allowed under 37 CFR 1.136(a))

(Note: Failure to file a Reply Brief will result in dismissal of the Appeal as to the claims made subject to an expressly stated new ground rejection.)

No fee is required for filing of this Reply Brief.

If any fees are required please charge Deposit Account 08-2025.

I hereby certify that this document is being  
transmitted to the Patent and Trademark Office  
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Respectfully submitted,  
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